

FP4-1

Analog Electronics & Actuators

Objectives:

To enable students to **apply basic knowledge of analog electronics and actuators for constructing hardware systems.**

Contents:

Analog Electronics

- o Diodes, PN-junction, small-signal model
- o Rectifiers, filtering and stabilization
- o Practical operational amplifier circuits, common-mode rejection ratio, slew-rate
- o Bipolar junction transistor basics, DC-analysis, signal amplification, small-signal model
- o Properties of the transistor
- o Frequency response of the transistor amplifier
- o MOSFET transistor

Electrical Actuators

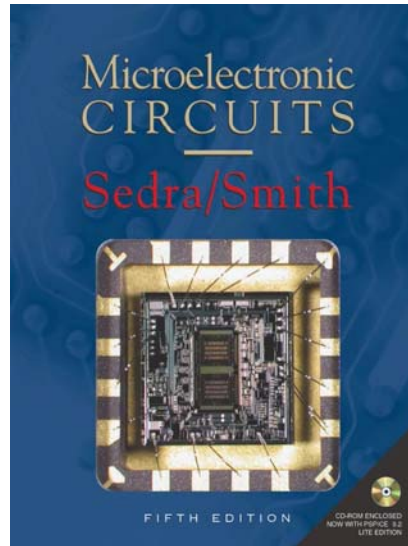
- o Electromechanical energy conversion. General principle, Work, forces, torque, efficiency etc. for electric machines.
- o DC-motors. DC-machine construction, characteristics, dynamic models and applications. Motor drivers

Course Web Page

- Contents**
- Schedule**
- Slides**
- Useful Links**
- Any other Matter & Related Material**

Microelectronic Circuits 5/e

Sedra/Smith



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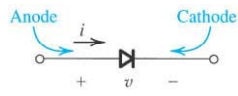
CHAPTER 3

Diodes

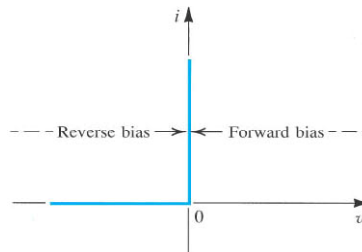
Lecture # 1

7

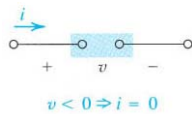
The Ideal Diode



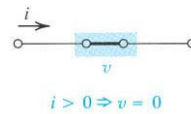
(a)



(b)



(c)



(d)

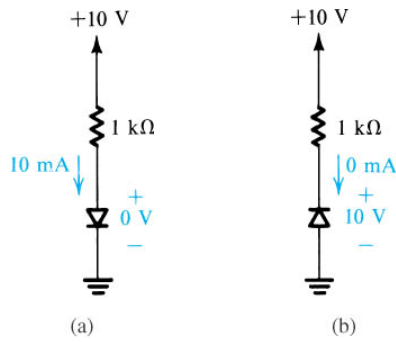
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Forward / Reversed Biased



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The Rectifier

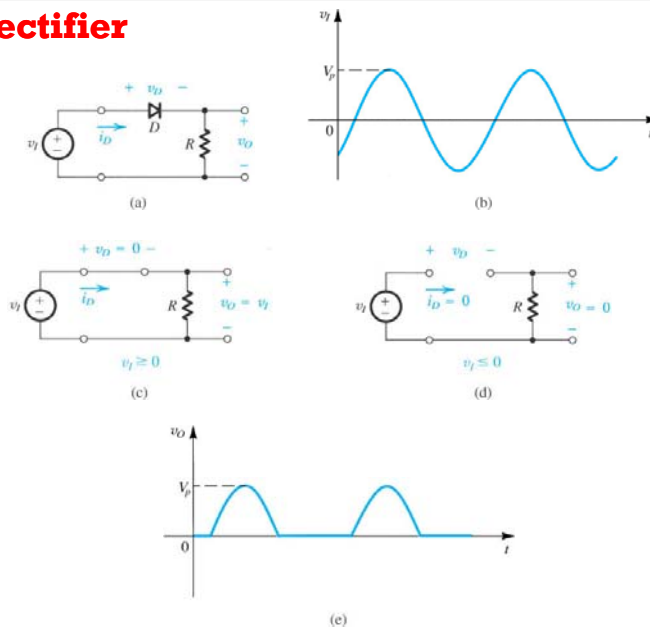


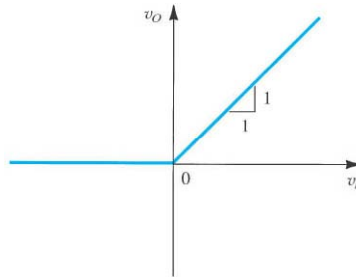
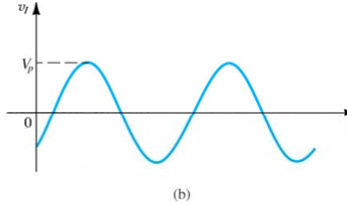
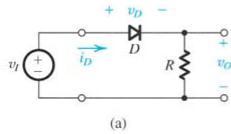
Figure 3.3 (a) Rectifier circuit. (b) Input waveform. (c) Equivalent circuit when $v_i \geq 0$. (d) Equivalent circuit when $v_i \leq 0$. (e) Output waveform.

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Exercise 3.1



For the circuit (a) sketch the transfer characteristics v_O versus v_I .



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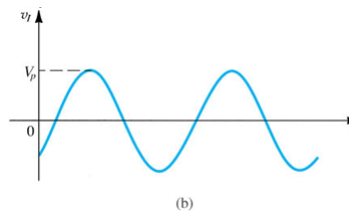
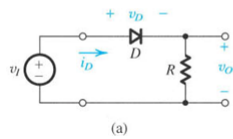
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11

Exercise 3.2 & 3.3



For the circuit (a) sketch the waveform of v_D .



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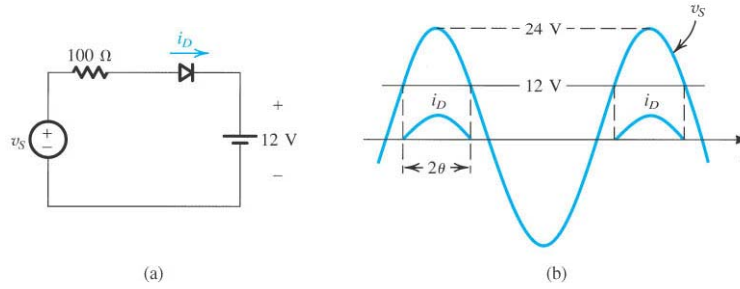
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12

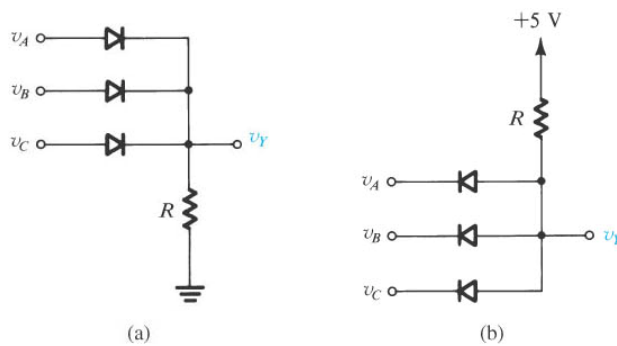
Example 3.1



For the circuit (a) find the fraction of the cycle during which diode conducts, peak value of the diode current and the maximum reverse bias voltage across diode.



Diode Logic Gates



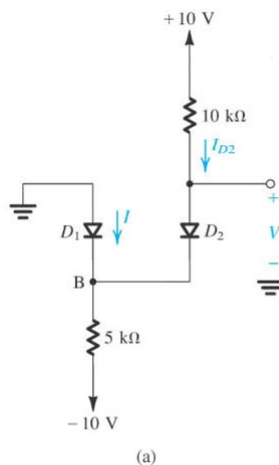
Example 3.2: Calculate V & I



$$I_{D2} = (10 - 0)/10 * 10^3 = 1 \text{ mA}$$

$$I + 1 = (0 - (-10))/5 * 10^3 = 2$$

$$I = 2 - 1 = 1 \text{ mA}$$



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15

Example 3.2: Calculate V & I



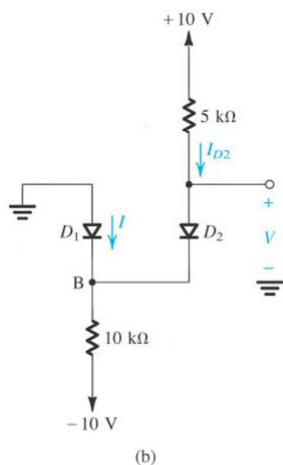
$$I_{D2} = (10 - 0)/5 * 10^3 = 2 \text{ mA}$$

$$I + 2 = (0 - (-10))/10 * 10^3 = 1$$

$$I = 1 - 2 = -1 \text{ mA}$$

$$I_{D2} = (10 - (-10))/15 * 10^3 = 1.33 \text{ mA}$$

$$V_B = -10 + (10 * 10^3 * 1.33 * 10^{-3}) = 3.3 \text{ V}$$



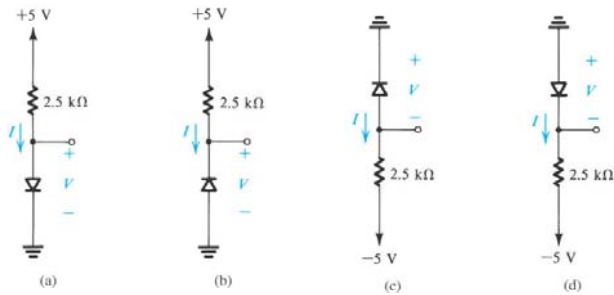
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16

Exercise 3.4: Calculate V & I



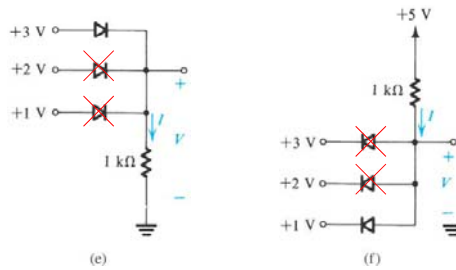
(a)	(b)	(c)	(d)
2.0 mA 0 V	0.0 mA 5 V	0.0 mA 5 V	2.0 mA 0 V

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17

Exercise 3.4: Calculate V & I



(e)	(f)
3.0 mA 3 V	4.0 mA 1 V

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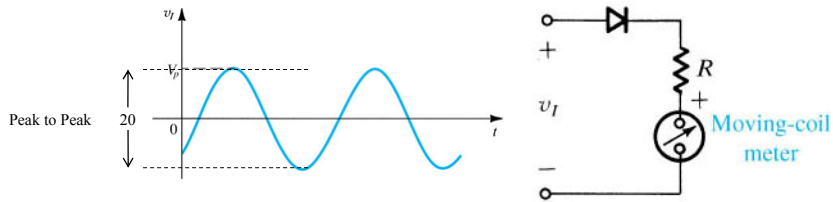
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18

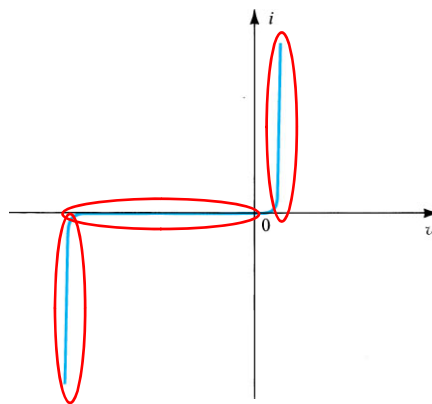
Exercise 3.5:



Calculate R , that result full scale reading when the input sine wave voltage is 20 V p-p. The meter gives full scale reading when the average current flowing through it is 1 mA, coil has 50 ohm resistance.
(Hint: The average value of half wave rectifier is V_p/π .)



Terminal Behaviour/Characteristics



***i-v* Characteristics**

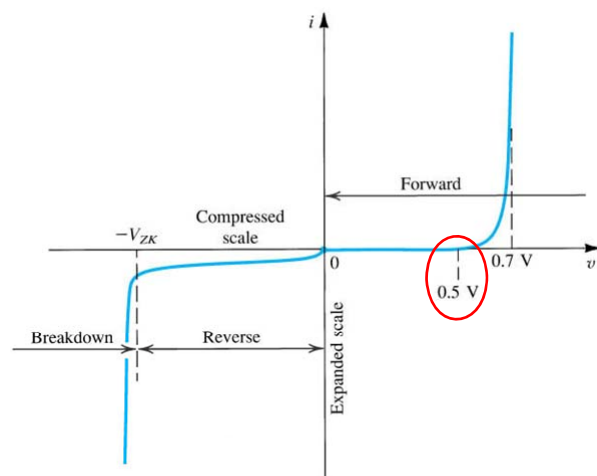
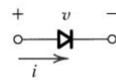
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21

Forward Bias Region



$$i = I_S(e^{v/nV_T} - 1)$$

$$V_T = \frac{kT}{q}$$

$$i \approx I_S e^{v/nV_T}$$

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22

Exercises: 3.6, 3.7 & 3.8



- 3.6: Consider a silicon diode with $n = 1.5$, Find the change in voltage if the current changes from 0.1 mA to 10 mA.
- 3.7: A silicon junction diode with $n = 1$ has $v = 0.7$ V at $i = 1$ mA. Find the voltage drop at $i = 0.1$ mA and $i = 10$ mA.
- 3.8: Using the fact that silicon diode as $I_S = 10^{-14}$ A at 25° C and I_S increases by 15 % per °C rise in temperature, find the value of I_S at 125° C.

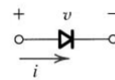
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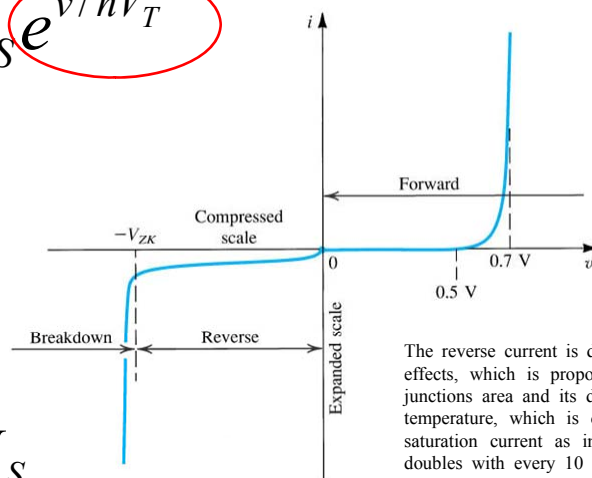
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23

Reverse Bias Region



$$i \approx I_S e^{v/nV_T}$$



$$i \approx -I_S$$

The reverse current is due to leakage effects, which is proportional to the junctions area and its dependence on temperature, which is different from saturation current as in this case it doubles with every 10 degree rise in temperature.

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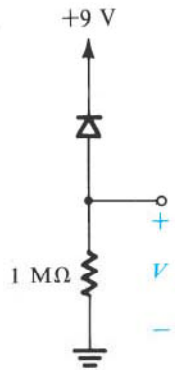
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24

Exercise 3.9



If $V = 1$ v at 20°C , find the value of V at 40°C and at 0°C .



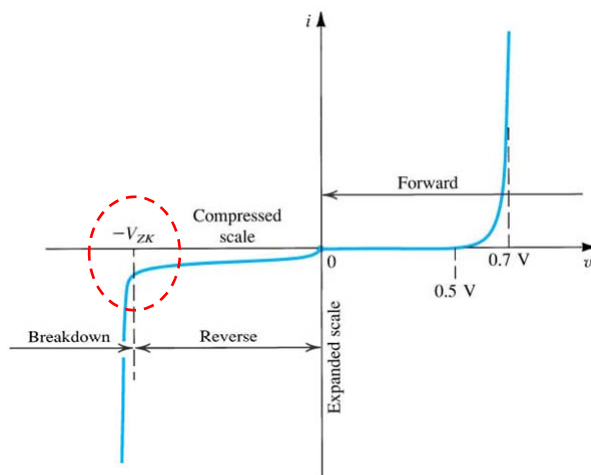
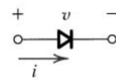
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Breakdown Region



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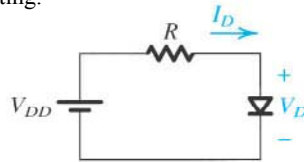
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26

Modelling Forward Characteristics



Following simple circuit used to illustrate the analysis of circuits in which the diode is forward conducting.



Exponential Model is the most accurate, but the trouble is it is severely ?

Assuming V_{DD} greater than cut in voltage 0.5, so

$$I_D = I_S e^{V_D/nV_T}$$

Applying Kirchhoff loop equation:
$$I_D = \frac{V_{DD} - V_D}{R}$$

Both equations have 2 unknown quantities ?

So how to get to the solution ? **Graphical or Iterative**

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27

Graphical Analysis Using Exponential Model

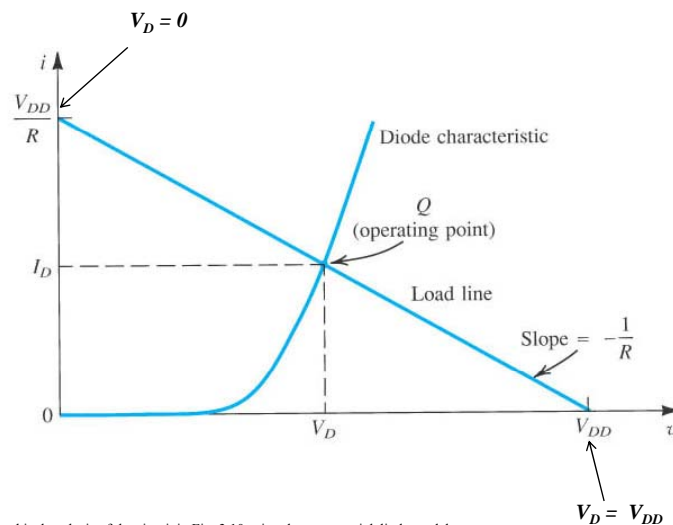


Figure 3.11 Graphical analysis of the circuit in Fig. 3.10 using the exponential diode model.

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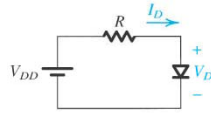
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28

Iterative Analysis Using Exponential Model



Suppose $V_{DD} = 5\text{ V}$, $R = 1\text{ K}\Omega$ in the above circuit to determine I_D and V_D using iterative analysis. Assume diode current 1 mA at a voltage of 0.7, which changes by 0.1 V for every decade change in current.



$$I_D = \frac{V_{DD} - V_D}{R}$$

$$V_2 - V_1 = 2.3 * n * V_T \log \left(\frac{I_2}{I_1} \right)$$

$$V_2 = V_1 + 0.1 \log \left(\frac{I_2}{I_1} \right)$$

$$2.3 * n * V_T = 0.1$$

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29

Rapid Analysis



- ❑ **What is the advantage of Rapid Analysis ?**
- ❑ **Why we do Rapid Analysis ?**

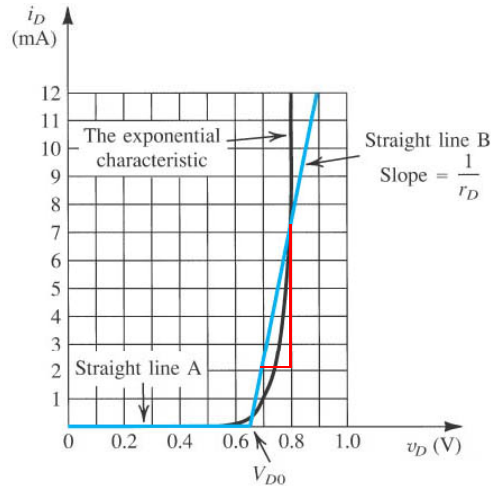
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30

Piecewise Linear Model



Difference is about 50 mV
(over current range 0.1 mA – 10 mA)

$$i_D = 0, v_D \leq V_{D0} \text{ (straight line A)}$$

$$i_D = (v_D - V_{D0})/r_D, v_D \geq V_{D0} \text{ (straight line B)}$$

$$\text{Slope} = Y/X = 5 \text{ mA} / 0.1 \text{ V} = 0.05$$

$$r_D = 1/\text{Slope} = 20 \Omega$$

$$V_{D0} = 0.65 \text{ V}$$

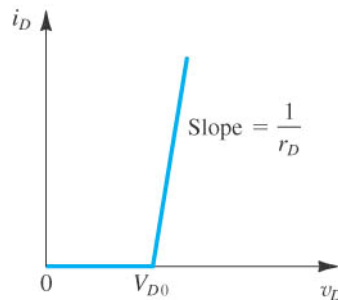
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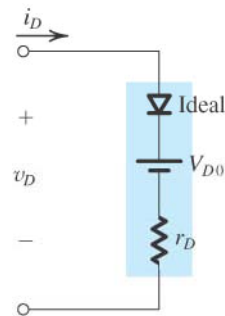
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31

Equivalent Circuit for Piecewise Linear Model



(a)



(b)

$$i_D = (V_D - V_{D0})/r_D$$

Battery-Plus Resistance Model

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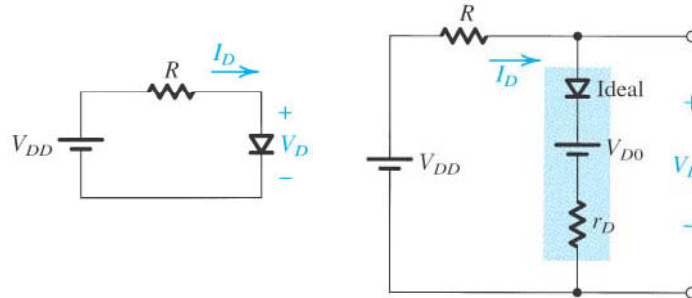
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32

Example 3.5



Suppose $R = 1 \text{ k}\Omega$, $V_{DD} = 5 \text{ V}$, $V_{D0} = 0.65 \text{ V}$, $r_D = 20 \Omega$.



$$i_D = (V_{DD} - V_{D0}) / R + r_D$$

$$V_D = V_{D0} + I_D r_D$$

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33

Constant Voltage Drop Model

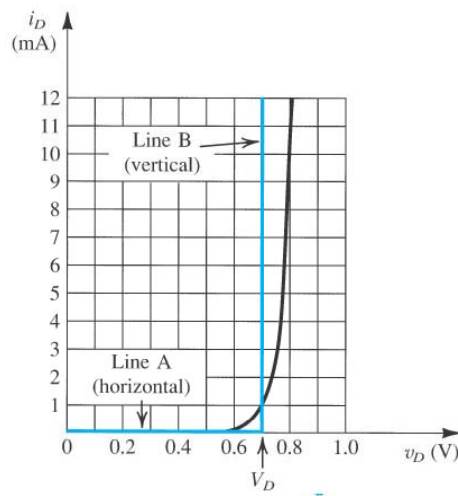


Figure 3.15 Development of the constant-voltage-drop model of the diode forward characteristics. A vertical straight line (B) is used to approximate the fast-rising exponential. Observe that this simple model predicts V_D to within $\pm 0.1 \text{ V}$ over the current range of 0.1 mA to 10 mA .

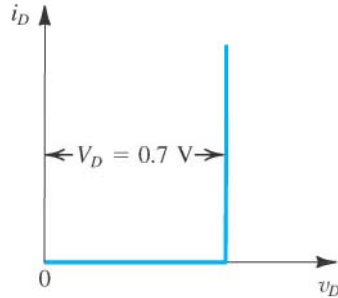
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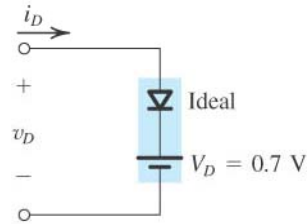
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34

Equivalent Circuit



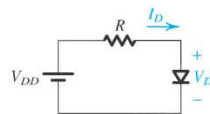
(a)



(b)

$$i_D = (V_{DD} - V_{D0})/R$$

$$= (5.0 - 0.7)/1 = 4.3 \text{ mA}$$



(which is not different what we had for piecewise linear model)

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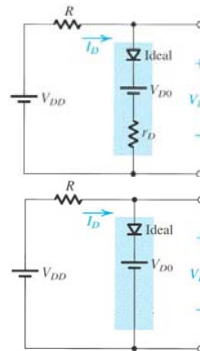
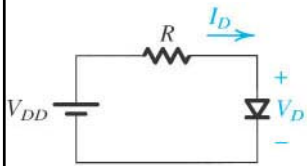
35

Exercises: 3.10



Suppose $R = 10 \text{ k}\Omega$, $V_{DD} = 5 \text{ V}$, Assuming that the diode has a voltage drop of 0.7 V at a current 0.1 mA and the voltage changes by 0.1 V/decade of current change. Use the following to calculate I_D and V_D .

- (a): Iteration
- (b): Piecewise linear model $V_{D0} = 0.65$, $r_D = 20 \Omega$.
- (c): Constant voltage model with $V_D = 0.7 \text{ V}$.



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Exercises: 3.11



Consider a diode that is 100 times as large (in junction area), if we approximate the characteristics as shown below over a range of current 100 times as large, how would the model parameters V_{D0} and r_D change.

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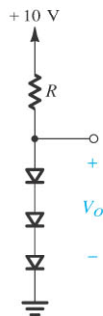
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37

Exercises: 3.12



Design the following circuit to provide an output voltage of 2.4 V, assuming the voltage drop across diodes is 0.7 V at 1 mA and that $\Delta V = 0.1$ V/decade change in current.



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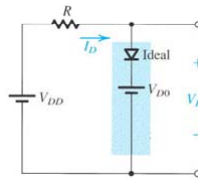
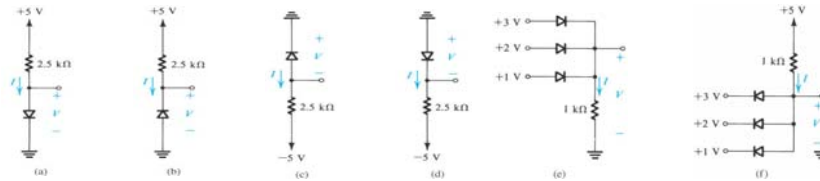
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38

Exercises: 3.13



Use the constant voltage drop model (0.7 V) on the following circuits to obtain better estimates of current (I) and voltage (V).



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39

Home work:

Problem: 3.1, 3.2, 3.3, 3.4, 3.5, 3.7, 3.8



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40